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FIG. 1

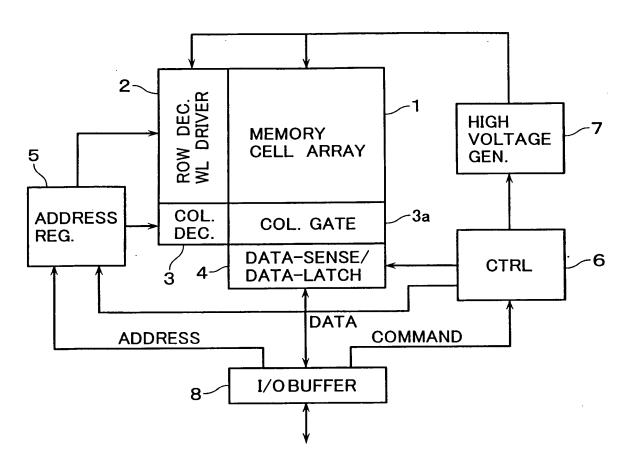
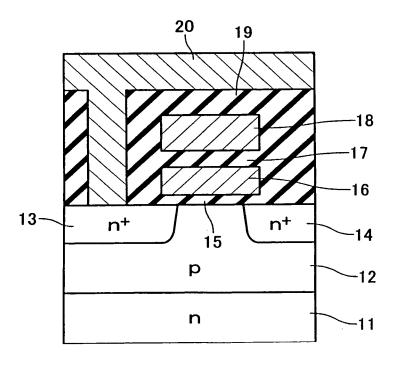
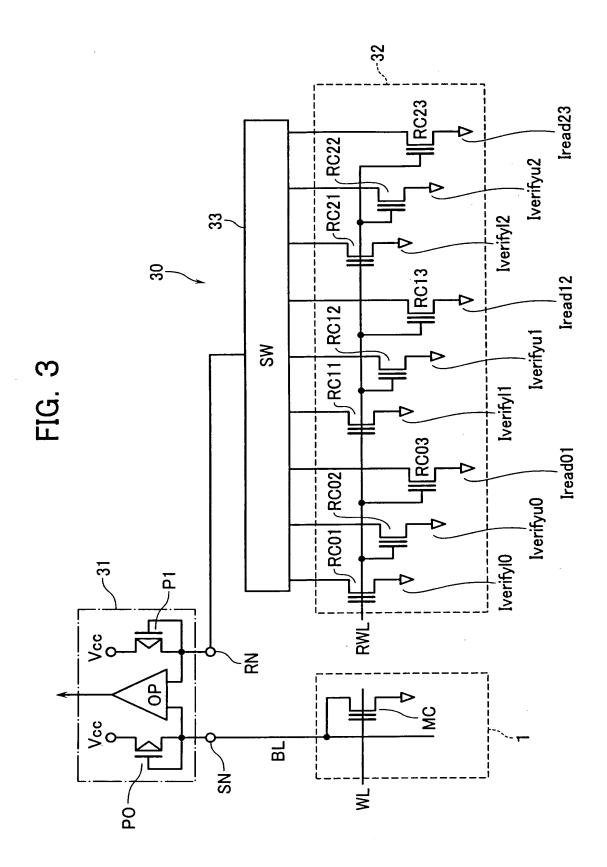
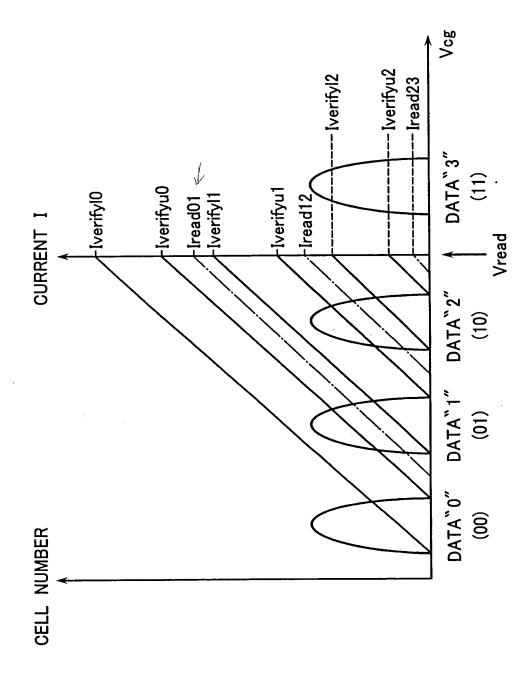


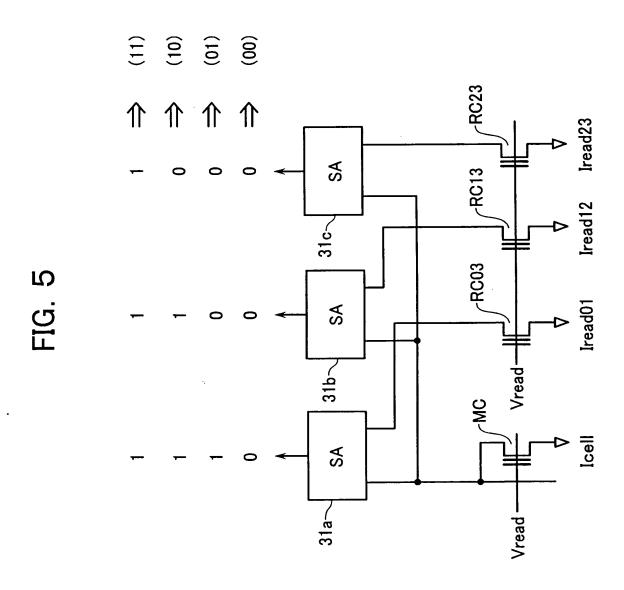
FIG. 2







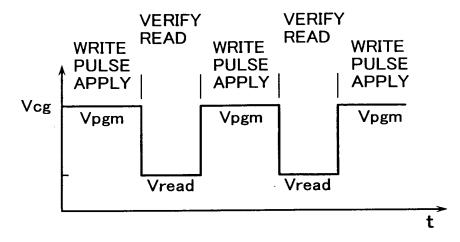




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FIG. 6

## MEMORY CELL



## REFERENCE CELL

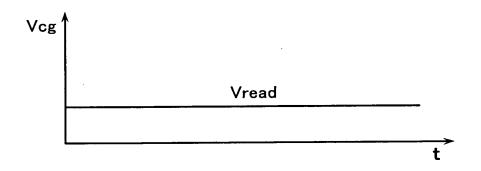
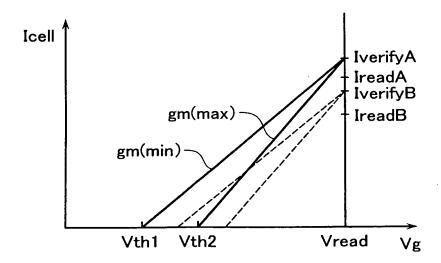


FIG. 7



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FIG. 8

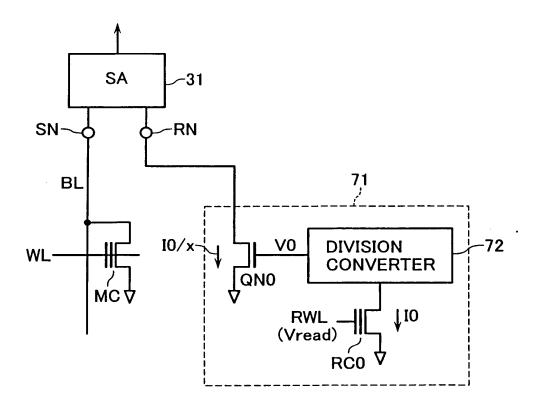
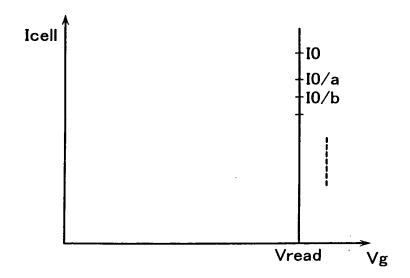
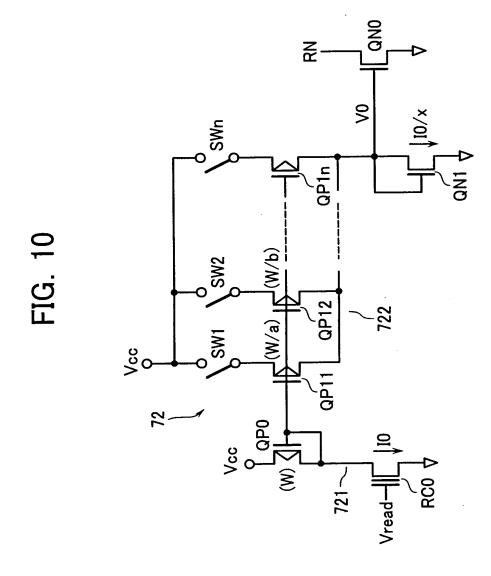


FIG. 9





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FIG. 11

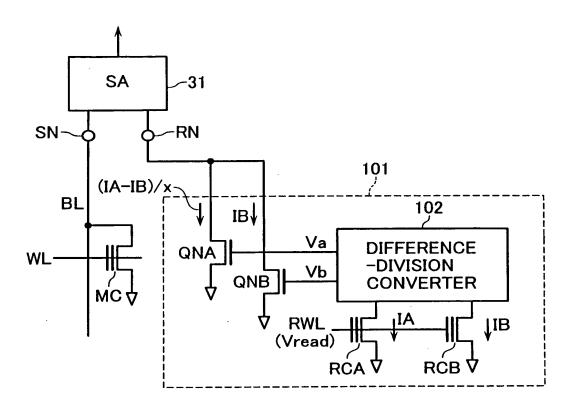
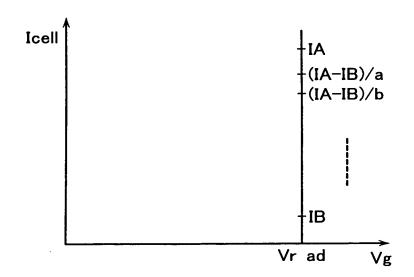
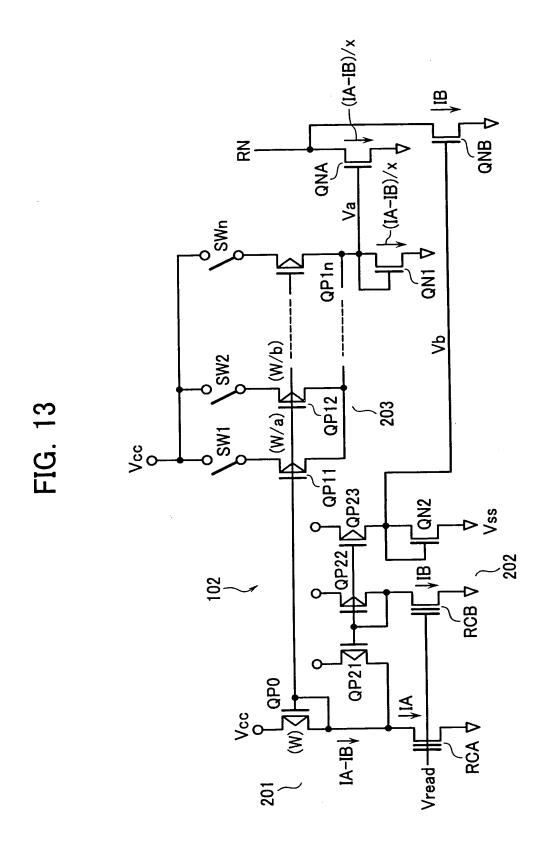


FIG. 12



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FIG. 14

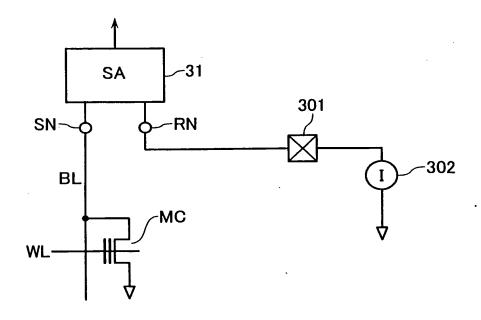
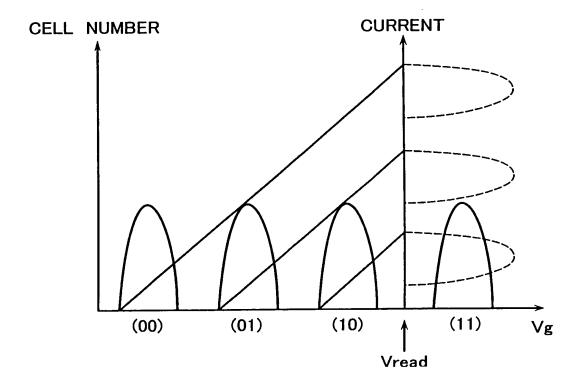


FIG. 15



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FIG. 16

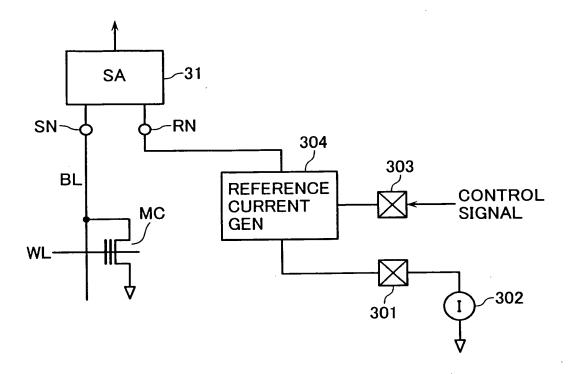
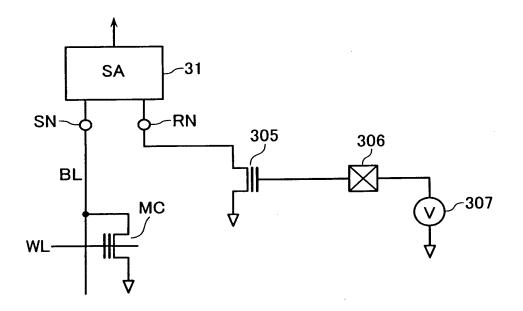


FIG. 17



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FIG. 18

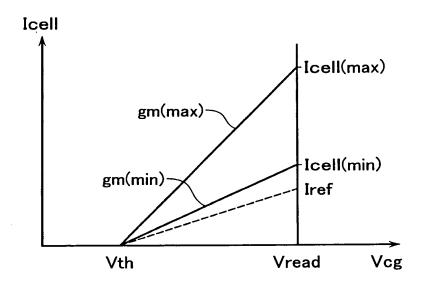


FIG. 19

